

WEST Search History

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DATE: Friday, April 02, 2004

<input type="checkbox"/> Hide?	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
		<i>DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L26	((6032274) [ABPN1,NRPN,PN,TBAN,WKU]) and @pd > 20040206 (L23 and clock edge with (compress\$3 or compact\$3) same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7 or stor\$4 or register\$4)) and @pd > 20040206	0
<input type="checkbox"/>	L25	(clock edge) and @pd > 20040206	0
<input type="checkbox"/>	L24	(L22 and clock edge with (compress\$3 or compact\$3)) and @pd > 20040206	0
<input type="checkbox"/>	L23	(L18 and L19) and @pd > 20040206 (L18 and (compress\$4 or compact\$4) same (pull\$1up or pull\$1down)same latch\$4 same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7) and (clock or timing)same (test\$3 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4)) and @pd > 20040206	158
<input type="checkbox"/>	L22	(L17 and (compress\$4 or compact\$4) same (pull\$1up or pull\$1down)same latch\$4 same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7) and (clock or timing)same (test\$3 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4)) and @pd > 20040206	0
<input type="checkbox"/>	L21	(L17 and (compress\$4 or compact\$4) same (pull\$1up or pull\$1down)same latch\$4 same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7) and (clock or timing)same (test\$3 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4)) and @pd > 20040206	0
<input type="checkbox"/>	L20	(L17 and (compress\$4 or compact\$4) same (pull\$1up or pull\$1down)same latch\$4 same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7) and (clock or timing)same (test\$3 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4)) and @pd > 20040206	0
<input type="checkbox"/>	L19	(L16 and (compress\$4 or compact\$4) same (pull\$1up or pull\$1down)same latch\$4 same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7) and (clock or timing)same (test\$3 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4)) and @pd > 20040206	0
<input type="checkbox"/>	L18	(L16 and (compress\$4 or compact\$4) same (pull\$1up or pull\$1down)same latch\$4 same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7) and (clock or timing)and @pd > 20040206	0
<input type="checkbox"/>	L17	((compress\$4 or compact\$4) and (pull\$1up or pull\$1down)same latch\$4) and @pd > 20040206	21
<input type="checkbox"/>	L16	(L4 and (pull\$1up or pull\$1down)same latch\$4) and @pd > 20040206	0
<input type="checkbox"/>	L15	(L10 and (pull\$1up or pull\$1down)same latch\$4) and @pd > 20040206	1
<input type="checkbox"/>	L14	((pull\$1up or pull\$1down)same latch\$4) and @pd > 20040206	60
<input type="checkbox"/>	L13	((6163491) [ABPN1,NRPN,PN,TBAN,WKU]) and @pd > 20040206 (L10 and double data rate same (compress\$4 or compact\$4) same (test\$3 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4) same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7) and (clock or timing)) and @pd > 20040206	0
<input type="checkbox"/>	L12	(L9 and double data rate and (compress\$4 or compact\$4) same (test\$3 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4) same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7) and (clock or timing)) and @pd > 20040206	0
<input type="checkbox"/>	L11	(L9 and double data rate and (compress\$4 or compact\$4) same (test\$3 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4) same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7) and (clock or timing)) and @pd > 20040206	0

h e b b cg b chh e hh g f ff e ch e g e

<input type="checkbox"/>	L10	(double data rate and (compress\$4 or compact\$4) same (test\$3 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4) same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7)) and @pd > 20040206	4
<input type="checkbox"/>	L9	(L4 and (double near (data or rate) or ddr) same (compress\$4 or compact\$4) same (test\$3 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4) and (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7)) and @pd > 20040206	0
<input type="checkbox"/>	L8	(('6324602') [ABPN1, NRPN, PN, TBAN, WKU]) and @pd > 20040206	0
<input type="checkbox"/>	L7	(('6317372') [ABPN1, NRPN, PN, TBAN, WKU]) and @pd > 20040206	0
<input type="checkbox"/>	L6	(L4 and (double near (data or rate) or ddr) same (compress\$4 or compact\$4) same (test\$3 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4) and (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7)) and @pd > 20040206	0
<input type="checkbox"/>	L5	((double near (data or rate) or ddr) same (compress\$4 or compact\$4)) and @pd > 20040206	10
<i>DB=PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD; PLUR=YES; OP=ADJ</i>			
<input type="checkbox"/>	L4	('6324602')!.PN.	2
<input type="checkbox"/>	L3	double near2 (data or rate) same (compress\$3 or compact\$3) same (edge or trailing or leading) with clock\$3	1
<input type="checkbox"/>	L2	double near (data or rate) with (compress\$3 or compact\$3) same clock\$3	3
<input type="checkbox"/>	L1	double data rate with (compress\$3 or compact\$3) same clock\$3	0

END OF SEARCH HISTORY